COMPUTER SCIENCE / BOOLEAN ALGEBRA

COMPU

Taking the cases when S = 1, an expression for S can be formed from the truth table:

$$S = \overline{P}.\overline{X}.\overline{Y} + \overline{P}.\overline{X}.\overline{Y} + P.\overline{X}.\overline{Y} + P.X.\overline{Y}$$

Using the rules we have learned we can simplify this expression:

$$S = P.(X.Y + X.Y) + P.(X.Y + X.Y)$$

(distributive law)
$$S = P.(X.Y + X.Y) + P.(\overline{X.Y + X.Y})$$

(de Morgan)

Similarly, we can form an expression for C. From the truth table:

$$C = P.X.Y + P.X.Y + P.X.Y + P.X.Y$$

$$C = X.Y.(P+P) + P.(X.Y + X.Y)$$

(distributive law)

$$C = X.Y + P.(X.Y + X.Y)$$

(P+P=1)

Note that $\overline{X}.Y + X.Y$ is the sum output from a half adder circuit. Thus, a full adder circuit can be designed from two half adders.

EXERCISE 3

1) Simplify these expressions:

a) A.
$$(\overline{A} + \overline{B})$$

b) X + Y.(X + Y) + X.(
$$\overline{X}$$
 + Y)

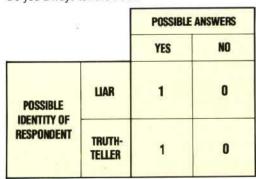
c) $P.Q + \overline{P}.Q + \overline{P}.\overline{Q}$

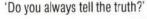
d) $\overline{X} + \overline{Y}.\overline{Z} + \overline{Z}.\overline{Y}$

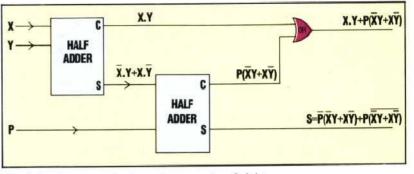
2) A car alarm has an on/off switch and switches on the two front doors. The alarm will sound if either or both doors are opened when the on/off switch is set to on. Draw a truth table showing the three inputs (door A, door B, and the on/off switch) and the alarm output. Use your truth table to write a Boolean expression for the alarm sounding and draw a logic circuit for the alarm system.

3) A hall light is operated from a switch at the door, a switch at the bottom of the stairs or one at the top of the stairs. Design a suitable logic circuit.

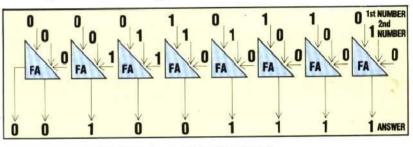
4) You are marooned on a desert island with two other people. One of these people always speaks the truth, the other always tells lies. For your own survival it is imperative that you find out which one will tell the truth. There are a number of questions that you could ask one of them to determine his or her identity. Draw up truth tables to investigate the possible answers. Here is an example to start you off.

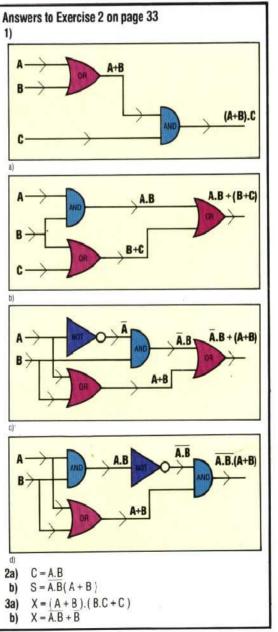






The following example shows how a series of eight full adders combine inside the ALU to perform the binary addition of two eight-bit numbers.





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