expressed as \$08+\$FB. The result of this sum is \$103, which is \$03 as a single-byte number.

This kind of representation is known as two's complement: the complement of a single-byte number is formed by subtracting it from \$100. There is another representation known as one's complement, and the two are related in an interesting way. Consider this:

\$05 = 00000101 \$FA = 11111010 +1	binary one's complement
SFB = 11111011	two's complement
\$05+\$FA=\$FF \$05-\$FB=\$00	

The one's complement of a single-byte number is formed simply by complementing or negating each binary bit of the number. If one is added to this result, then the two's complement of the number is produced. A number and its one's complement always total SFF, while a number and its two's complement always total \$00 (actually \$100). It is conventional then, in signed integer arithmetic, to regard the numbers from \$00 to \$7F as the positive numbers, (0 to 127) and \$80 to SFF as the negative numbers (-128 to -1). If you compare the binary representations of these numbers you will notice that all the negative integers have bit 7 set, while in the positive numbers bit 7 is always reset. Accordingly, bit 7 is known as the sign bit of a signed number, and the carry flag of the processor status register is set or reset as a copy of bit 7 of the result of the last arithmetic or logical operation.

There is no easy way round this potentially confusing subject, and it simply has to be approached when you start doing signed arithmetic. Fortunately, once its implications are understood, it can be handled mechanically by rule-of-thumb methods. These methods, and the multiplication and division alogrithms, are the subject of the next instalment of the course.

register onto the stack. This means that the characters Answers To Exercises On Page 259 at which protocols and a second and the model of the

i) the tonowing	program reverses the order of	me
character string	stored at LABL1:	
and the second		

register onto the stack. This means that the characters
of the string at LABL1 are interspersed on the stack
with successive values of the processor status register.
780

MESSAGE'

		6502			Z80
£				ORG	\$000
ORIGIN	ORG	\$7000	LAST1		\$00
LAST1	EQU	\$0D	LABL1	DB	THIS IS A
LABL1	DB	'THIS IS A MESSAGE'	TERMN8	DB	LASTI
TERMNS	DB	LAST1	1		
			BEGIN	LD	IX,LABL1-1
BEGIN	LDX	林季FF		LD	A, LAST1
	LDA	#LAST1		PUSH	AF
	PHA		LOOPO	INC	IX
LOOPO	INX			LD	A,(1X+0)
	LDA	LABL1,X		PUSH	AF
	PHA			CP	LAST1
	CMP	#LAST1	ENDLPO	JR	NZ,LOOPO
ENDLPO	BNE	LOOPO	CLRSTK	POP	AF
CLRSTK	PLA		;		
1		And the second s	BEGIN1	LD	IX,LABL1-1
BEGIN1	LDX	秋 争FF	LOOP1	INC	IX
LOOP1	INX		11-12-12-12-12-12-12-12-12-12-12-12-12-1	POP	AF
	PLA			LD	(IX+0),A
	STA	LABL1,X		CP	LAST1
	CMP	#LAST1	ENDLP1	JR	NZ LOOP1
ENDLP1	BNE	LOOP1		RET	
	RTS				
	10021102				

In the 6502 version, the code between LOOP0 and ENDLOOPO uses X-indexed addressing in a loop to load the characters one-by-one from LABL1, and push them onto the stack - having first pushed the ASCII value of the terminator character to mark the bottom of the stack. The last character pushed onto the stack is also the terminator, this time determined from its position as the last character in the string. This concludes the loop, and the terminate character on top of the stack is then cleared at CLRSTK.

The Z80 version uses IX in indirect addressing mode to load the accumulator from LABL1 onwards. and pushes not only the accumulator but also the flag

The code between BEGIN1 and ENDLP1 in both versions is a reflection of the previous loop and uses the same techniques, but this time pulling the character string off the stack in reverse order, and storing it at LABL1 onwards. The loop finishes when the terminator character is found at the bottom of the stack.

Notice how important it is to balance stack pushes and pulls, and that the most difficult part of the problem is deciding how to handle the extreme conditions - what to do at the start of the loops, how to terminate them, and what 'tidying-up' (if any) is then required